Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"20020002654" .PN.	US-PGPUB; USPAT	OR	ON	2006/04/25 06:44
L2	1	"20020002654".PN.	US-PGPUB; USPAT	OR	ON.	2006/04/25 06:46
L3	1590	711/103.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/25 06:46
L4	980	711/103.ccls.	USPAT	OR	ON	2006/04/25 07:12
L5	4	("6229731" "5974513" "6543017" "5374847").pn.	USPAT	OR	ON	2006/04/25 08:17
L6	1	security adj release adj key	US-PGPUB; USPAT	OR	ON .	2006/04/25 08:53
L7	1	"5708715".pn. and key	US-PGPUB; USPAT	OR	ON	2006/04/25 09:38
L18	17	(semiconductor or semi-conductor) adj storage adj device and (non-volatile or nonvolatile) adj memory and EEPROM and secur\$3 and switch\$3 and circuit	US-PĞPUB; USPAT	OR	ON	2006/04/25 09:39
S1	789	semiconductor adj storage adj device	USPAT	OR	ON	2004/11/08 17:49
S2	31	semiconductor adj storage adj device same secur\$3	USPAT	OR	ON	2004/11/08 11:35
S3	316	semiconductor adj storage adj device and @py<"2000"	USPAT	OR	ON	2004/11/08 11:58
S4	278	semiconductor adj storage adj device and @py<"1999"	USPAT	OR	ON	2004/11/08 11:48
S5	173	semiconductor adj storage adj device and non-volatile	USPAT	OR	ON.	2004/11/08 11:49
S6	165	semiconductor adj storage adj device and non-volatile same memory	USPAT	OR	ON	2004/11/09 08:13
S8 _.	20	semiconductor adj storage adj device and non-volatile same memory and secur\$3	USPAT	OR	ON	2004/11/08 11:53
S9	40	semiconductor adj storage adj device and @py<"2000" and secur\$3	USPAT	OR	ON	2004/11/08 13:24
S10	22	semi-conductor adj storage adj device	USPAT	OR	ON	2004/11/08 13:21
S11	153	semiconductor and storage and device and (non-volatile or nonvolatile) and EEPROM and secur\$3 and memory and switch\$3 adj circuit	USPAT	OR	ON	2004/11/08 13:26
S12	971	semiconductor and storage and device and (non-volatile or nonvolatile) and EEPROM and secur\$3 and memory and switch\$3 and circuit	USPAT	OR	ON	2004/11/08 13:26
S13	15	(semiconductor or semi-conductor) adj storage adj device and (non-volatile or nonvolatile) and EEPROM and secur\$3 and memory and switch\$3 and circuit	USPAT	OR	ON	2004/11/08 15:13
S14	5	("5872734" "5898630" "5901084" "5999444" "6285592"). PN. •	US-PGPUB; USPAT; USOCR	OR ·	ON	2004/11/08 13:55
S16	7	("4821240" "4864542" "5377343" "5396471" "5406519" "5559992" "5838613").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 13:58
S17	5	("5872734" "5898630" "5901084" "5999444" "6285592"). PN.	US-PGPUB; USPAT; USOCR	OR ·	ON	2004/11/08 14:50
S18	7	(semiconductor or semi-conductor) adj storage adj device and (non-volatile or nonvolatile) adj memory and EEPROM and secur\$3 and switch\$3 and circuit	USPAT	OR	ON	2006/04/25 09:37
S23	. 59	(semiconductor or semi-conductor) adj (storage or memory) adj device same (non-volatile or nonvolatile) adj (storage or memory) same circuit same switch\$3 same memory	USPAT	OR	ON	2004/11/08 16:25
S28	4	(semiconductor or semi-conductor) adj (storage or memory) adj device same (non-volatile or nonvolatile) adj (storage or memory) same circuit same switch\$3 same memory and secur\$3	USPAT	OR	ON	2004/11/08 16:25
S30	18	((semiconductor adj storage adj device) or (semiconductor adj memory adj device)) and secur\$3 and (non-volatile or nonvolatile) and (rewrit\$4 or re-writ\$4) and array and key	USPAT	OR	ON	2004/11/08 16:35

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S32	7	((semiconductor adj storage adj device) or (semiconductor adj memory adj device)) and secur\$3 and (non-volatile or nonvolatile) and (rewrit\$4 or re-writ\$4) and array and key and circuit and ("713"/\$.ccls. or "709"/\$.ccls. or "380"/\$.ccls. or "711"/\$.ccls.)	USPAT	OR	ON	2004/11/08 16:36
S33	. 5	("4716856" "5829009" "6330648" "6389542" "6397301"). PN	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 16:37
S34	8	("5452442" "5537540" "5684736" "5715274" "5745734" "5805712" "5896574" "5918008").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 16:59
S35	.8	("5452442" "5537540" "5684736" "5715274" "5745734" "5805712" "5896574" "5918008").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 17:02
S36	14	("3798360" "4004089" "4484025" "4751733" "4914697" "5666411" "5692047" "5752035" "5835592" "5847577" "5867507" "5890189" "5898783" "5963980").PN.	US-PGPUB; USPAT; USOCR	OR	ON .	2004/11/08 17:06
S37	224	713/201.ccls. and (semiconductor or semi-conductor)	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 17:07
S38	116	713/201.ccls. and (semiconductor or semi-conductor or semi adj conductor)	USPAT	OR	ON	2004/11/08 17:08
S39	5	713/201.ccls. and ((semiconductor or semi-conductor or semi adj conductor) adj (storage or memory) adj device)	USPAT	OR	ON	2004/11/08 17:09
S42	34	semiconductor adj (storage or memory) adj device same memory same non-volatile same switch\$3 adj circuit	USPAT	OR	ON	2004/11/08 17:32
S43	4	semiconductor adj (storage or memory) adj device same memory same non-volatile same switch\$3 adj circuit and secur\$3	USPAT	OR	ON	2004/11/08 17:34
S44	4	semiconductor adj (storage or memory) adj device same memory same non-volatile same switch\$3 adj circuit and (secur\$3 or authent\$6)	USPAT	OR	ON	2004/11/08 17:34
S45	8	("5452442" "5537540" "5684736" "5715274" "5745734" "5805712" "5896574" "5918008").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 17:39
S46	` 5	("4864542" "5155829" "5175840" "5216633" "5377343"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/08 17:46
S47	8	("5838613").URPN.	USPAT	OR	ON	2004/11/08 17:47
S48	312	semiconductor adj (storage or memory) adj device same secur\$3	USPAT	OR .	ON	2004/11/08 17:50
S49	· 9	semiconductor adj (storage or memory) adj device same secur\$3 same (rewrit\$6 or re-writ\$6)	USPAT ·	OR	ON	2004/11/08 17:53
S50	312	semiconductor adj (storage or memory) adj device same secur\$3	USPAT	OR	ON	2004/11/08 17:53
S51	. 47	semiconductor adj (storage or memory) adj device same secur\$3 same nonvolatile	USPAT	OR .	ON	2004/11/08 17:53
S52	. 62	semiconductor adj (storage or memory) adj device same secur\$3 same (nonvolatile or non-volatile)	USPAT	OR	ON	2004/11/08 17:53
S53	27	("4203158" "4266628" "4279024" "4376947" "4412309" "4437174" "4460982" "4661833" "4698750" "4758986" "4763305" "4797856" "4805151" "4811294" "4878203" "4888734" "4924437" "4939690" "4949309" "4958317" "4998220" "5032881" "5034922" "5053990" "5077691" "5175840" "5222046").PN.	US-PGPUB; USPAT; USOCR	OR .	ON	2004/11/08 17:59
S63	34	("4203158" "4266283" "4279024" "4291388" "4376947" "4412309" "4437174" "4460982" "4661833" "4698750" "4698787" "4742492" "4758986" "4763305" "4797856" "4805151" "4811294" "4878203" "4888734" "4924437" "4939690" "4949609" "4958317" "4958321" "4998220" "5032881" "5034922" "5043940" "5053990" "5077691" "5155701" "5175840" "5222046" "5602987").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/09 08:41

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S65	1257	semiconductor adj storage adj device	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/09 08:47
S66	585	semiconductor adj storage adj device same circuit	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/09 08:47
S67	. 791	semiconductor adj storage adj device	USPAT	OR	ON	2004/11/09 08:48
S68	144.	(semiconductor adj storage adj device.detd.) and (semiconductor adj storage adj device.ab.) and (semiconductor adj storage adj device.clm.)	USPAT	OR	ON	2004/11/09 08:49
S69	28	(semiconductor adj storage adj device.detd.) and (semiconductor adj storage adj device.ab.) and (semiconductor adj storage adj device.clm.) and secur\$3	USPAT	OR	ON	2004/11/09 08:49
S80	791	semiconductor adj storage adj device	USPAT	OR	ON	2004/11/09 11:41
S81	316	semiconductor adj storage adj device and @py<"2000"	USPAT	OR	ON	2004/11/09 11:41
S82	40	semiconductor adj storage adj device and @py<"2000" and secur\$3	USPAT	OR	ON	2004/11/09 12:41
S10 5	523	semiconductor adj (storage or memory) adj device same output same register	USPAT	OR	ON	2004/11/09 16:22
S10 7	15	semiconductor adj storage adj device same output same register	USPAT	OR	ON	2004/11/09 16:34
S10 8	3	semiconductor adj storage adj device same conversion adj circuit	USPAT	OR	ON	2004/11/09 16:39
S10 9	32	semiconductor adj storage adj device same register	ÚSPAT	OR	ON	2004/11/09 16:39
S11 0	11	semiconductor adj storage adj device same register and @py<"2000"	USPAT	OR	ON	2004/11/09 16:40
S11 3	6	semiconductor adj storage adj device same (output adj signal) and register	USPAT	OR	ON	2004/11/09 16:41
S11 4	2	semiconductor adj storage adj device same (output adj signal) same register	USPAT	OR	ON	2004/11/09 16:48
S11 6	653	security same release same key	USPAT	OR	ON	2004/11/09 16:49
S11 7	41	security same release adj key	USPAT	OR	ON	2004/11/09 16:49
S11 9	3	lack same security same release same key	USPAT	OR	ON	2004/11/09 16:52
S12 0	791	semiconductor adj storage adj device	USPAT	OR	ON	2004/11/09 16:53
S12 1	12	semiconductor adj storage adj device and one-time	USPAT	OR	ON	2004/11/09 16:57
S12 2	608	one-time adj (programmable or ROM)	USPAT	OR	ON	2004/11/09 16:57
S12 3	19	one-time adj (programmable adj ROM)	USPAT	OR	ON	2004/11/09 16:58
S12 [°] 6	51	semiconductor adj storage adj device same register	US-PGPUB; USPAT	OR	ON	2004/11/10 08:42
S12 7	24	semiconductor adj storage adj device same register same output	US-PGPUB; USPAT	OR	ON	2004/11/10 08:43
S12 8	15	semiconductor adj storage adj device same register same output	USPAT	OR	ON -	2004/11/10 08:50
S13 0	11	semiconductor adj storage adj device and output adj register	USPAT	OR	ON	2004/11/10 08:58
S13 2	9	semiconductor adj (storage or memory) adj device and (output same register same compar\$5 same key)	USPAT	OR	ON	2004/11/10 09:06
S13 3	259	semiconductor adj storage adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or shift register)	USPAT	OR '	ON	2004/11/10 09:07
S13 4	67	semiconductor adj storage adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or shift adj register)	USPAT	OR	ON	2004/11/10 09:08

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34	semiconductor adj storage adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or shift adj register) and @py<"2000"	USPAT	OR	ON .	2004/11/12 09:18
19	(one-time adj programmable adj ROM)	USPAT	OR	ON ·	2004/11/10 09:22
. 19	(one-time adj programmable adj ROM)	USPAT	OR	ON	2004/11/10 09:29
926	(one-time adj programmable adj ROM or OTPROM or OTP)	USPAT	OR	ON	2004/11/10 09:30
117	(one-time adj programmable adj ROM) or OTPROM	USPAT	OR	ON	2004/11/10 09:30
24	(one-time adj programmable adj ROM) or OTPROM and lock	USPAT	OR	ON	2004/11/10 09:31
6	((one-time adj programmable adj ROM) or OTPROM) and lock	USPAT	OR	ON	2004/11/10 09:31
16	semiconductor adj storage adj device and lack and @py<"2000"	USPAT	OR	ON	2004/11/10 09:51
15	semiconductor adj storage adj device and (lack and read\$4) and @py<"2000"	USPAT .	OR	ON	2004/11/10 09:52
46	semiconductor adj storage adj device same (delay or wait adj operation)	USPAT	OR	ON	2004/11/10 10:30
16	semiconductor adj storage adj device same (delay or wait adj operation) and @py<"2000"	USPAT	OR	ON	2004/11/10 10:30
142	(semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4)	US-PGPUB; USPAT	OR	ON	2004/11/10 10:36
41	(semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<"2000"	US-PGPUB; USPAT	OR	ON	2004/11/10 10:45
. 167	(semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py<"2000"	US-PGPUB; USPAT	OR	ON	2004/11/10 10:46
167	(semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py<"2000"	USPAT	OR	ON	2004/11/10 10:46
8	(semiconductor adj storage adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py<"2000"	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/10 10:48
. 2	(semiconductor adj storage adj device) same (wait\$4 and writ\$4) and @py<"2000"	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/10 10:50
301	(semiconductor adj (storage or memory) adj device) same ((wait\$4 or delay\$4) and writ\$4) and @py<"2000"	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/10 10:51
301	(semiconductor adj (storage or memory) adj device) same ((wait\$4 or delay\$4) and writ\$4) and @py<"2000"	USPAT	OR	ON	2004/11/10 10:51
62	(semiconductor adj (storage or memory) adj device) and read\$4 same (secur\$4 or data or key) same lack	US-PGPUB; USPAT	OR	ON	2004/11/10 14:01
1768	register same buffer and semiconductor adj (storage or memory) adj device	US-PGPUB; USPAT	OR	ON	2004/11/10 16:09
437	register same buffer same semiconductor adj (storage or memory) adj device	US-PGPUB; USPAT	ÖR	ON	2004/11/10 16:09
288	register same buffer same semiconductor adj (storage or memory) adj device	USPAT	OR	ON	2004/11/10 16:09
98	register same buffer same semiconductor adj (storage or memory) adj device and @py<"2000"	USPAT	OR	ON	2004/11/10 16:57
. 14	semiconductor adj storage adj device and (unidirectional adj conversion adj circuit or conversion adj circuit) and @py<"2000"	USPAT	OR	ON	2004/11/12 09:33
153	semiconductor adj memory adj device and (unidirectional adj conversion adj circuit or conversion adj circuit) and @py<"2000"	USPAT	OR	ON	2004/11/12 09:21
153	semiconductor adj memory adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or encryption adj circuit) and @py<"2000"	USPAT	OR	ON	2004/11/12 09:21
	19 19 926 117 24 6 16 15 46 16 142 41 167 167 8 2 301 301 62 1768 437 288 98 14	conversion adj circuit or conversion adj circuit or shift adj register) and @py="2000" (one-time adj programmable adj ROM) (one-time adj programmable adj ROM) (one-time adj programmable adj ROM) or OTPROM or OTP) (one-time adj programmable adj ROM) or OTPROM or OTP) (one-time adj programmable adj ROM) or OTPROM and lock ((one-time adj programmable adj ROM) or OTPROM and lock ((one-time adj programmable adj ROM) or OTPROM) and lock semiconductor adj storage adj device and tack and @py="2000" semiconductor adj storage adj device and (lack and read\$4) and @py="2000" semiconductor adj storage adj device same (delay or wait adj operation) semiconductor adj storage adj device same (delay or wait adj operation) semiconductor adj storage adj device same (delay or wait adj operation) semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 amd secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4 and secur\$3) and @py="2000" semiconductor adj storage adj device) same (wait\$4 and writ\$4 and @py="2000" semiconductor adj storage adj device) same (wait\$4	conversion adj circuit or conversion adj circuit or shift adj register) and @py<"2000" 19 (one-time adj programmable adj ROM) 19 (one-time adj programmable adj ROM) 117 (one-time adj programmable adj ROM) or OTPROM or OTP) 117 (one-time adj programmable adj ROM) or OTPROM 117 (one-time adj programmable adj ROM) or OTPROM or OTP) 118 (one-time adj programmable adj ROM) or OTPROM and lock 119 (one-time adj programmable adj ROM) or OTPROM and lock 120 (one-time adj programmable adj ROM) or OTPROM) and lock 130 semiconductor adj storage adj device and lack and @py<"2000" 141 semiconductor adj storage adj device ame (delay or wait adj operation) 142 (semiconductor adj storage adj device same (delay or wait adj operation) 143 semiconductor adj (storage or memory) adj device) same (waits4 and writs4) and @py<"2000" 144 (semiconductor adj (storage or memory) adj device) same (waits4 and writs4) and @py<"2000" 145 (semiconductor adj (storage or memory) adj device) same (waits4 and writs4) and @py<"2000" 146 (semiconductor adj (storage or memory) adj device) same (waits4 and writs4) and @py<"2000" 147 (semiconductor adj storage adj device) same (waits4 and writs4) and @py<"2000" 148 (semiconductor adj storage adj device) same (waits4 and writs4) and @py<"2000" 149 (semiconductor adj storage adj device) same (waits4 and writs4) and @py<"2000" 150 (semiconductor adj storage adj device) same (waits4 and writs4) and @py<"2000" 151 (semiconductor adj storage adj device) same (waits4 and writs4) and @py<"2000" 152 (semiconductor adj (storage or memory) adj device) same (waits4 or delays4) and writs4) and @py<"2000" 153 (semiconductor adj (storage or memory) adj device) same (waits4 or delays4) and writs4) and @py<"2000" 154 (semiconductor adj (storage or memory) adj device) and register same buffer same semiconductor adj (storage or memory) adj device 154 (register same buffer same semiconductor adj (storage or memory) adj device 155 (semiconductor adj storage adj device and (unidirectional adj convers	conversion adj circuit or conversion adj circuit or shift adj register) and @py<*2000" 19 (one-time adj programmable adj ROM) 19 (one-time adj programmable adj ROM) 19 (one-time adj programmable adj ROM) or OTPROM or OTP) 117 (one-time adj programmable adj ROM) or OTPROM USPAT OR 118 (one-time adj programmable adj ROM) or OTPROM and lock 119 (one-time adj programmable adj ROM) or OTPROM and lock 110 (one-time adj programmable adj ROM) or OTPROM and lock 111 (one-time adj programmable adj ROM) or OTPROM and lock 112 (semiconductor adj storage adj device and lack and @py<**2000" 113 semiconductor adj storage adj device and (lack and read\$4) and @py<**2000" 114 (semiconductor adj storage adj device same (delay or wait adj operation) and @py<**2000" 114 (semiconductor adj storage adj device same (delay or wait adj operation) and @py<**2000" 114 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 114 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 115 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 116 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 117 (semiconductor adj (storage or memory) adj device) same (wait\$4 and secur\$3) and @py<**2000" 118 (semiconductor adj (storage adj device) same (wait\$4 and writ\$4) and @py<**2000" 119 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 110 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 110 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 110 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 118 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 119 (semiconductor adj (storage or memory) adj device) same (wait\$4 and writ\$4) and @py<**2000" 110 (semiconductor adj (storag	conversion adj circuit or conversion adj circuit or shift adj register) and @py="2000" 19 (one-time adj programmable adj ROM) 19 (one-time adj programmable adj ROM) 10 (one-time adj programmable adj ROM) 117 (one-time adj programmable adj ROM) or OTPROM or OTP) 118 (one-time adj programmable adj ROM) or OTPROM 119 (one-time adj programmable adj ROM) or OTPROM and lock 10 ((one-time adj programmable adj ROM) or OTPROM and lock 110 ((one-time adj programmable adj ROM) or OTPROM) and lock 111 ((one-time adj programmable adj ROM) or OTPROM) and lock 112 ((one-time adj programmable adj ROM) or OTPROM) and lock 113 ((one-time adj programmable adj ROM) or OTPROM) and lock 114 ((one-time adj programmable adj ROM) or OTPROM) and lock 115 ((one-time adj programmable adj ROM) or OTPROM) and lock 116 ((one-time adj programmable adj ROM) or OTPROM) and lock 117 ((one-time adj programmable adj ROM) or OTPROM) and lock 118 ((one-time adj programmable adj ROM) or OTPROM) and lock 119 ((one-time adj programmable adj ROM) or OTPROM) and lock 110 ((one-time adj programmable adj ROM) or OTPROM) and lock 110 ((one-time adj programmable adj ROM) or OTPROM) and lock 111 ((one-time adj programmable adj ROM) or OTPROM and lock 112 ((one-time adj programmable adj ROM) or OTPROM and lock 115 ((one-time adj programmable adj ROM) or OTPROM and lock 116 ((one-time adj programmable adj ROM) or OTPROM 117 ((one-time adj programmable adj ROM) or OTPROM 118 ((one-time adj programmable adj ROM) or OTPROM 119 ((one-time adj programmable adj ROM) or OTPROM 110 ((one-time adj programmable adj ROM) or OTPROM 110 ((one-time adj programmable adj ROM) or OTPROM 111 ((one-time adj programmable adj ROM) or OTPROM 112 ((one-time adj programmable adj ROM) or OTPROM 112 ((one-time adj programmable adj ROM) or OTPROM 114 ((one-time adj programmable adj ROM) or OTPROM 115 ((one-time adj programmable adj ROM) or OTPROM 116 ((one-time adj programmable adj ROM) or OTPROM 117 ((one-time adj programmable adj ROM) or OTPROM 118 (

S19 0	573	semiconductor adj (memory or storage) adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or encryption adj circuit)	US-PGPUB; USPAT	OR	ON	2004/11/12 09:22
S19 1	407	semiconductor adj (memory or storage) adj device and (unidirectional adj conversion adj circuit or conversion adj circuit or encryption adj circuit)	USPAT	OR	ON	2004/11/12 09:22
\$19 4	8	semiconductor adj (storage or memory) adj device and (circuit same encrypt\$4) and @py<"2000"	USPAT	OR	ON	2004/11/12 10:04
S19 8	3	semiconductor adj (storage or memory) adj device and lock adj function	USPAT	OR	ON	2004/11/12 10:04
S20 0	14	("4864542" "5175840" "5335206" "5374847" "5539689" "58386 13" "5966388" "5974513" "6163484" "6222779" "6229731" "626 0172" "6415007" "6543017").PN.	US-PGPUB; USPAT	OR	ON	2005/07/28 08:14